Cloud based Analog IC Design Hackathon

This is to certify that

BEMALKHED AKHILESH

from
B.V. RAJU INSTITUTE OF TECHNOLOGY

has designed the circuit

Implementation Of 2:1 Mux Using CMOS

using Synopsys Custom Compiler Platform

She/He has performed an ~~Outstanding/Excellent/~~**Very Good**~~/ Good~~ work.

This program was conducted between 15 February - 1 March 2022 as an initiative of IIT Hyderabad, which has been sponsored by Synopsys in association with VLSI System Design (VSD) Pvt.

Dr Ashudeb Dutta
Program Co-Ordinator, IIT Hyderabad

Dr B Umashankar
Chair CCE, IIT Hyderabad

This is computer generated certificate. To verify visit https://hackathoniith.in